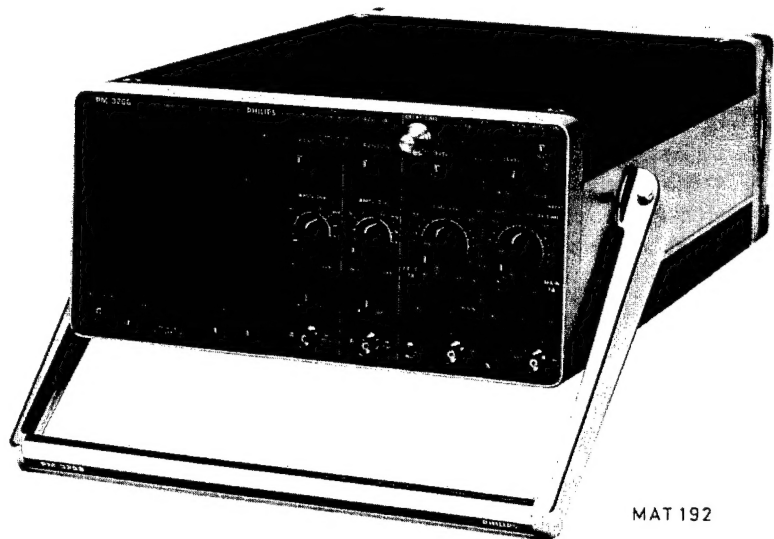


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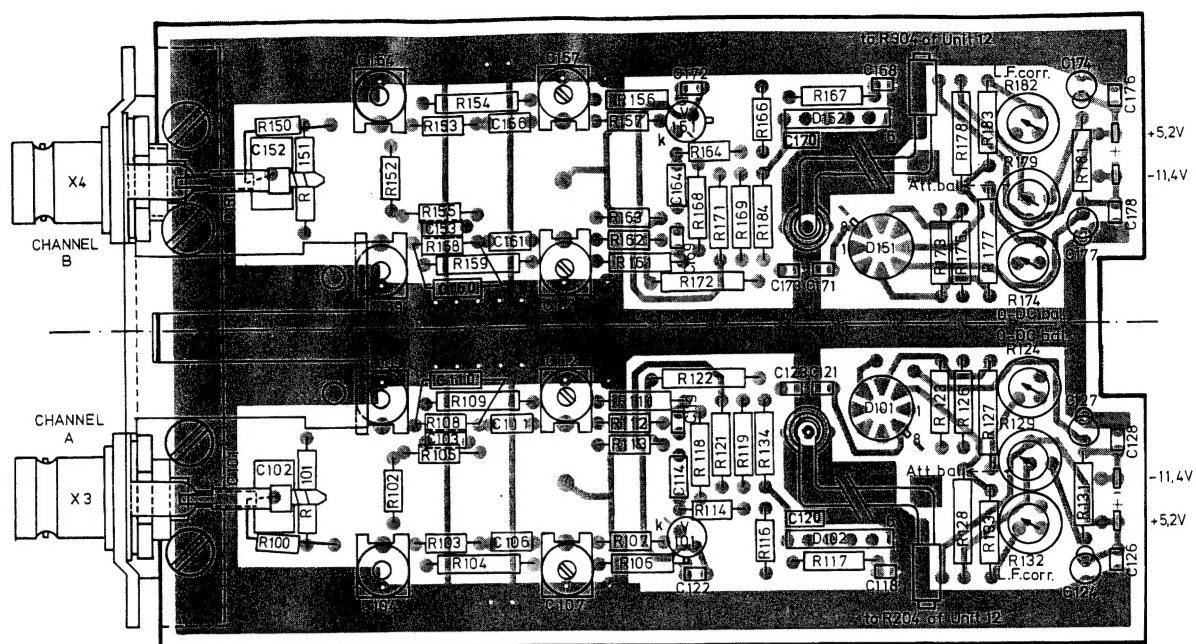
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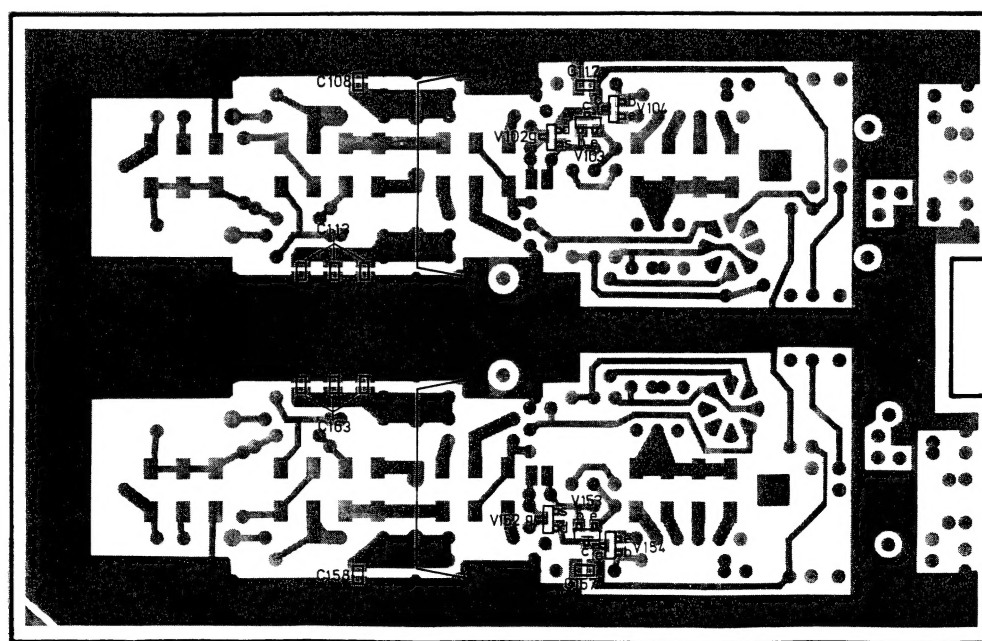
PHILIPS

12. CIRCUIT DIAGRAMS AND PRINTED CIRCUIT BOARD LAY-OUTS



MAT 47 A

Fig. 12.1. Vertical attenuator (Unit 2), component side lay-out



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Fig. 12.2. Vertical attenuator (Unit 2), conductor lay-out

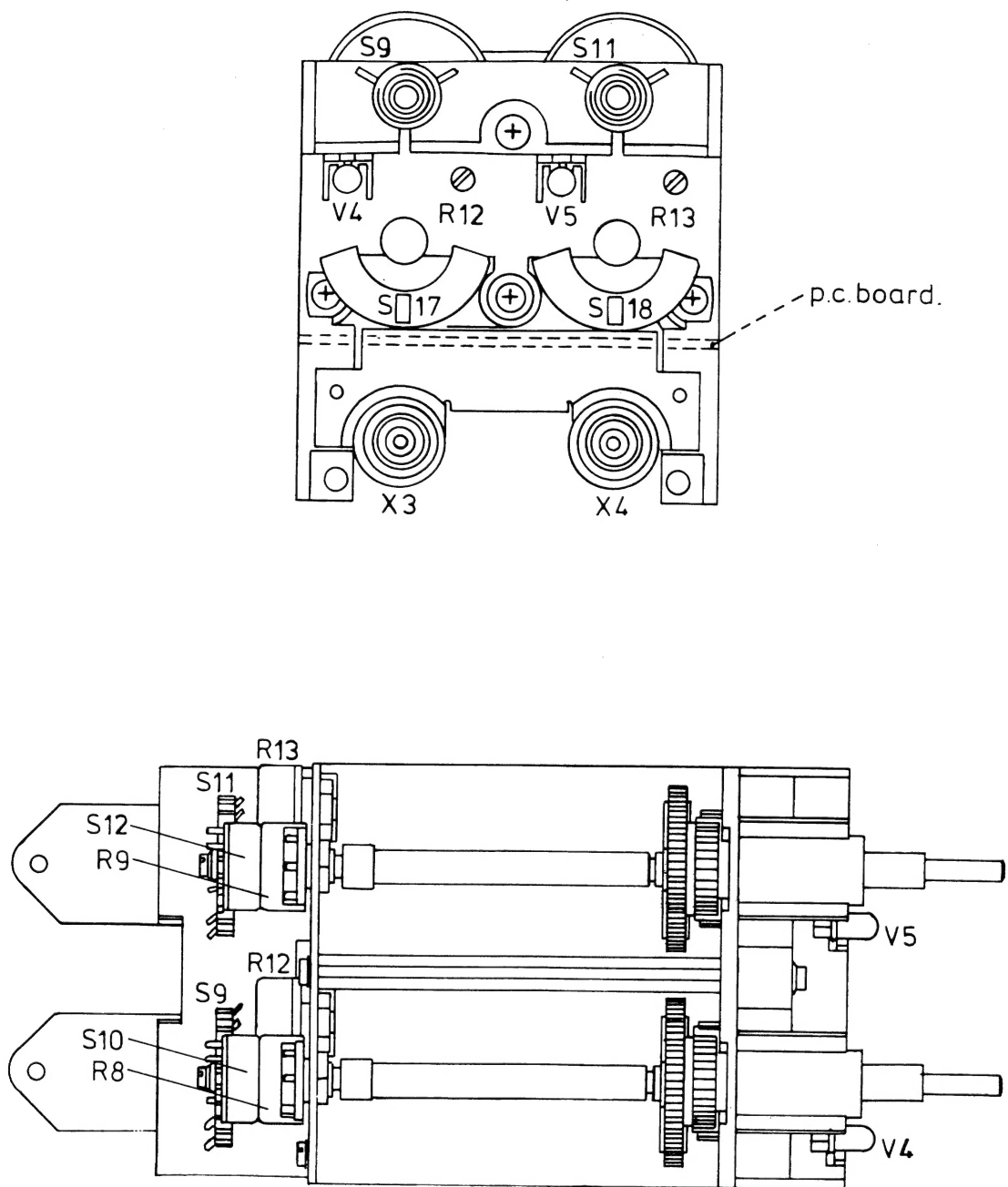


Fig. 12.3. Vertical attenuator (Unit 2), mechanical lay-out

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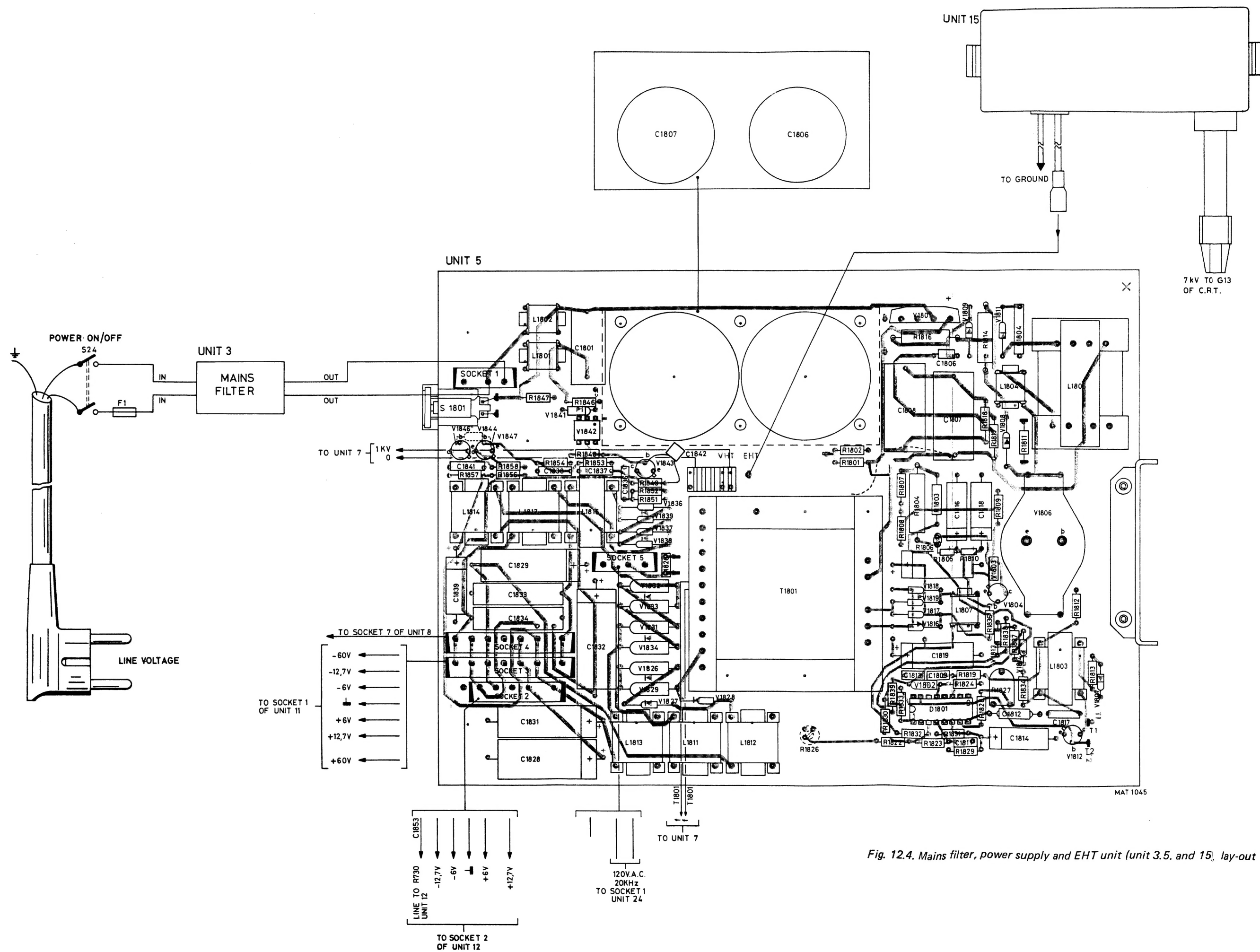


Fig. 12.4. Mains filter, power supply and EHT unit (unit 3.5. and 15), lay-out

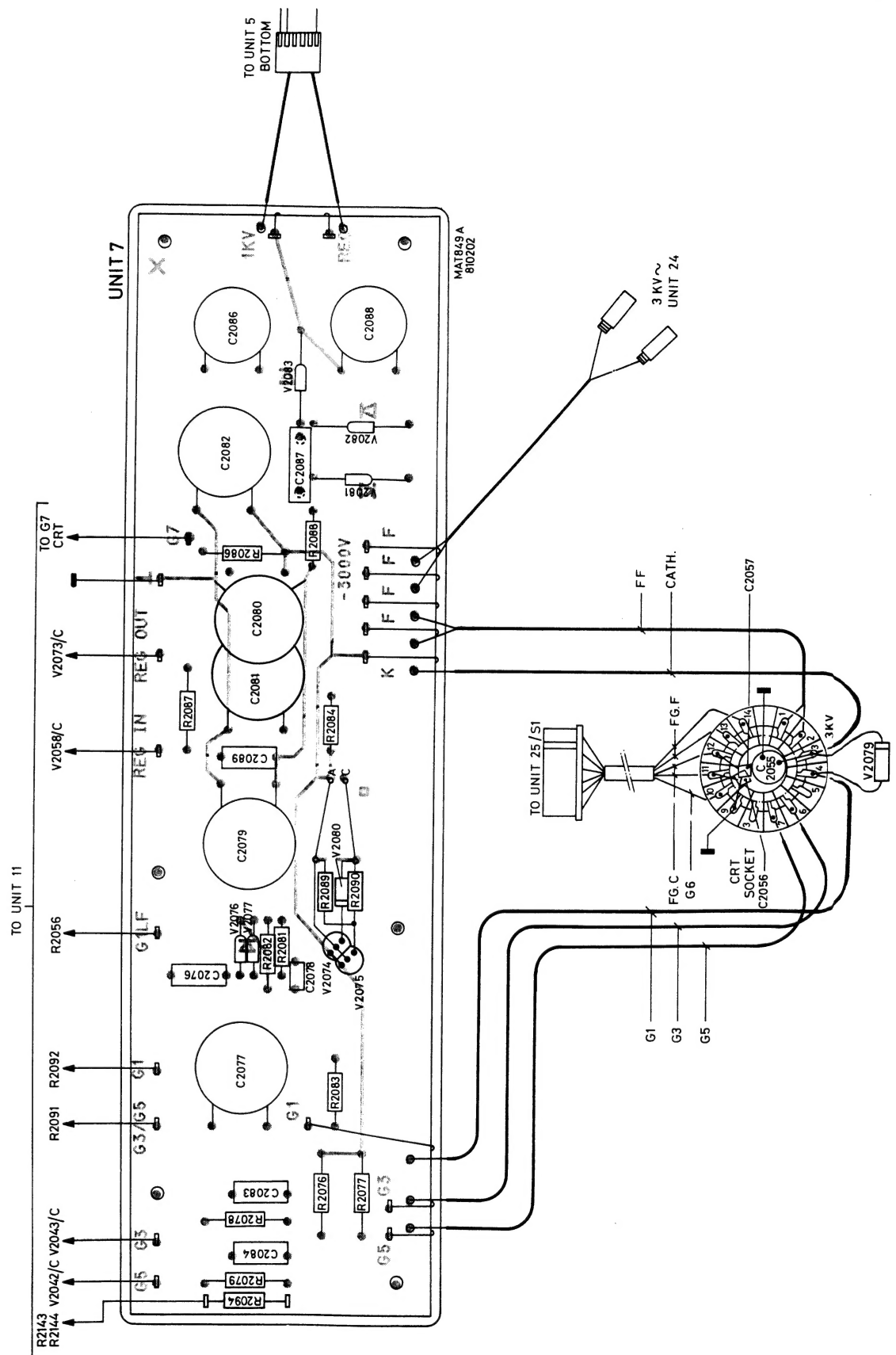


Fig. 12.5. Focus unit (unit 7, lay-out)

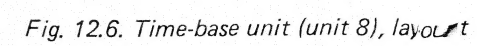


Fig. 12.6. Time-base unit (unit 8), layout

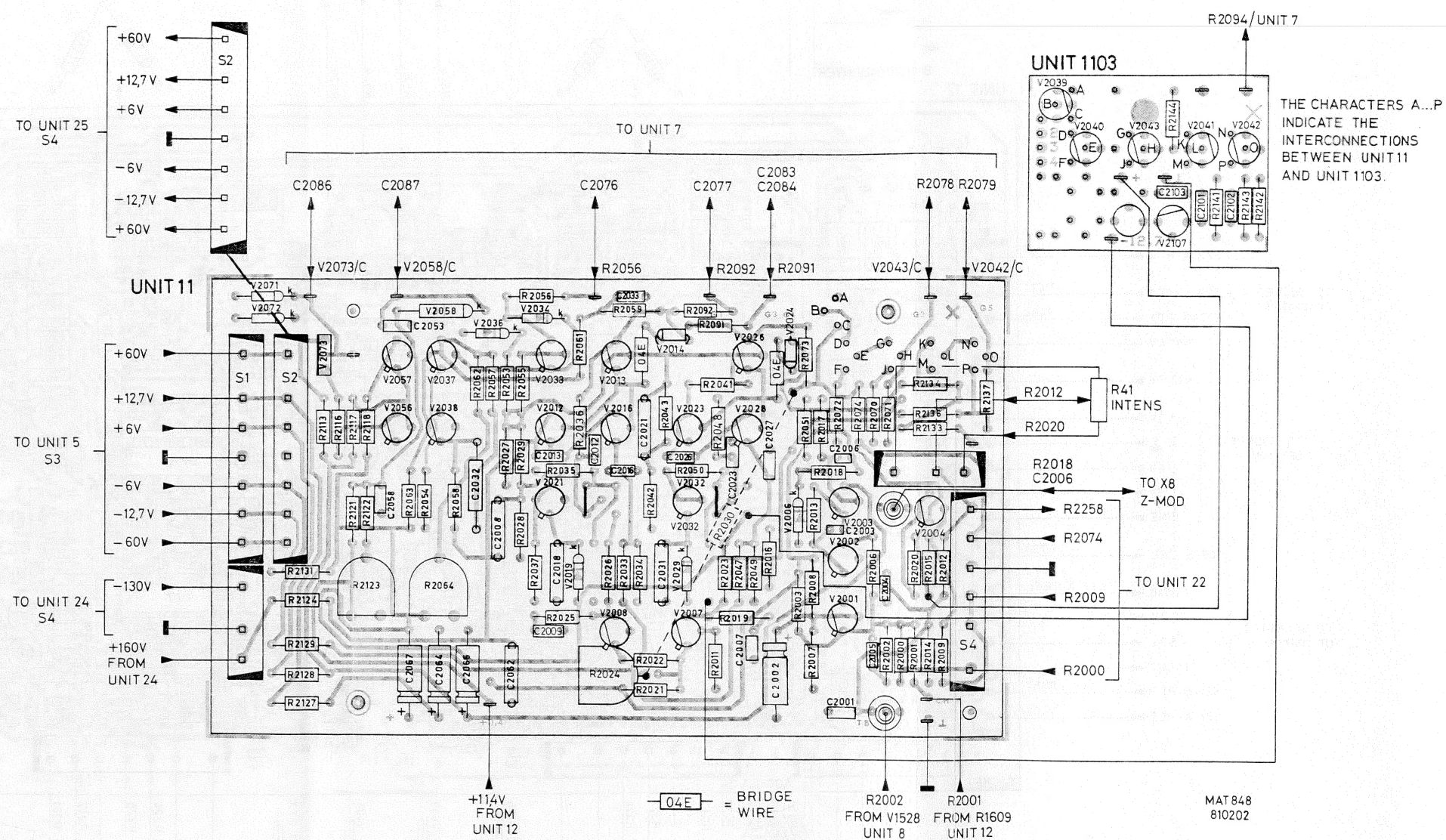


Fig. 12 8. Z-amplifier (unit 11), lay-out

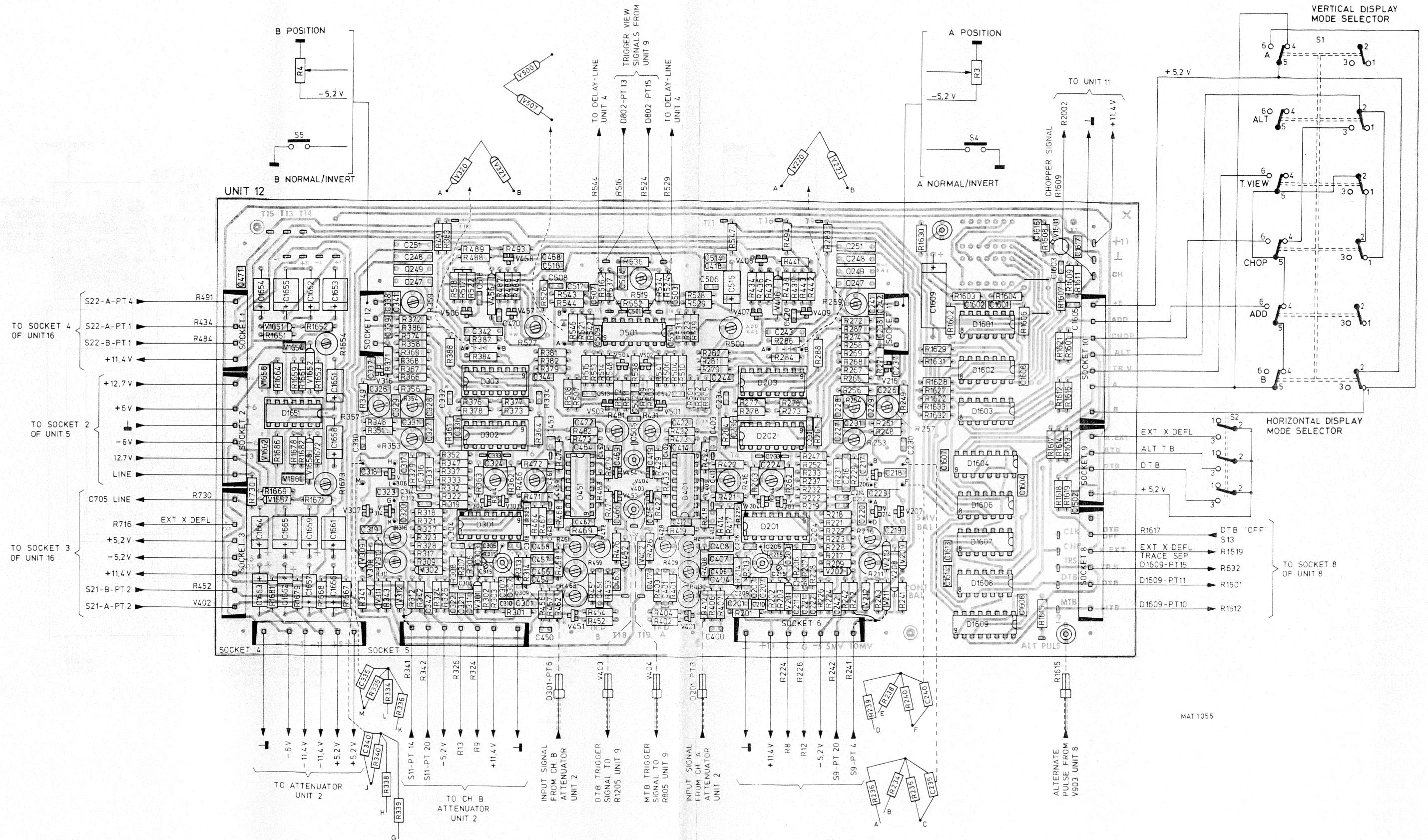


Fig. 12.9. Intermediate amplifier (unit 12), lay-out

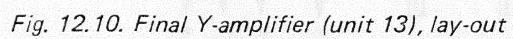


Fig. 12.10. Final Y-amplifier (unit 13), lay-out

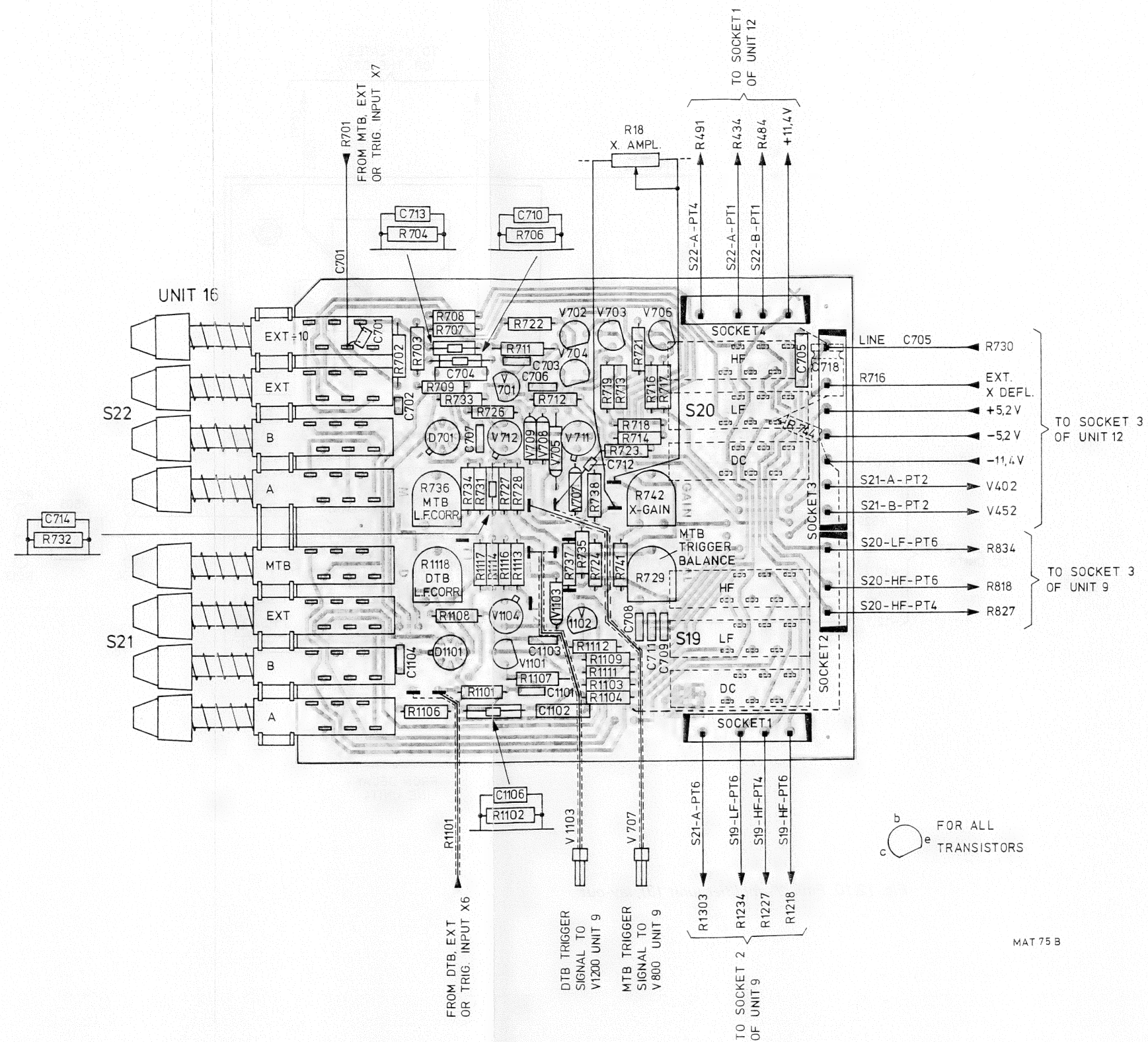


Fig. 12.11. Trigger source unit (unit 16), lay-out

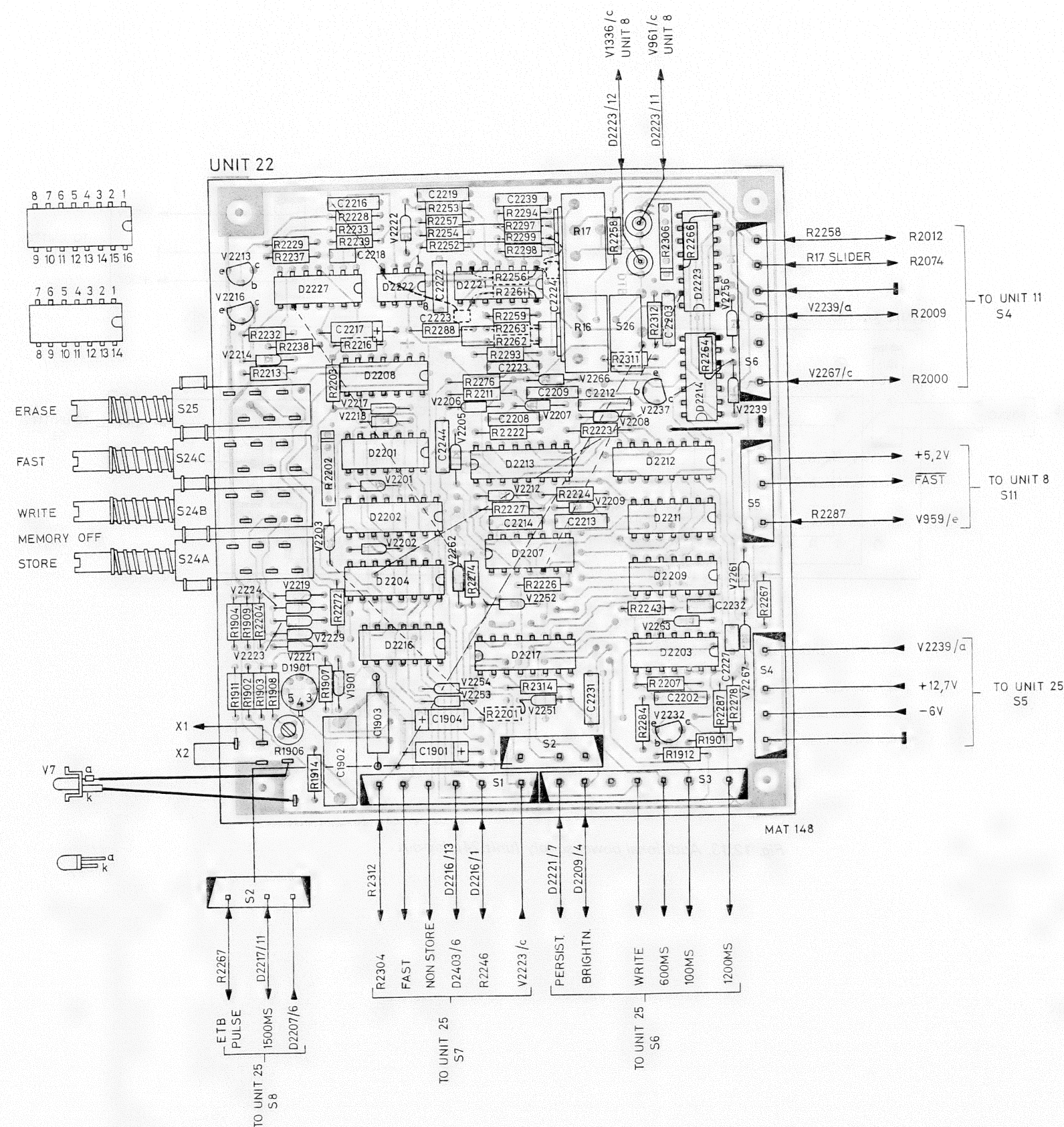


Fig. 12.12. Storage logic (unit 22), lay-out

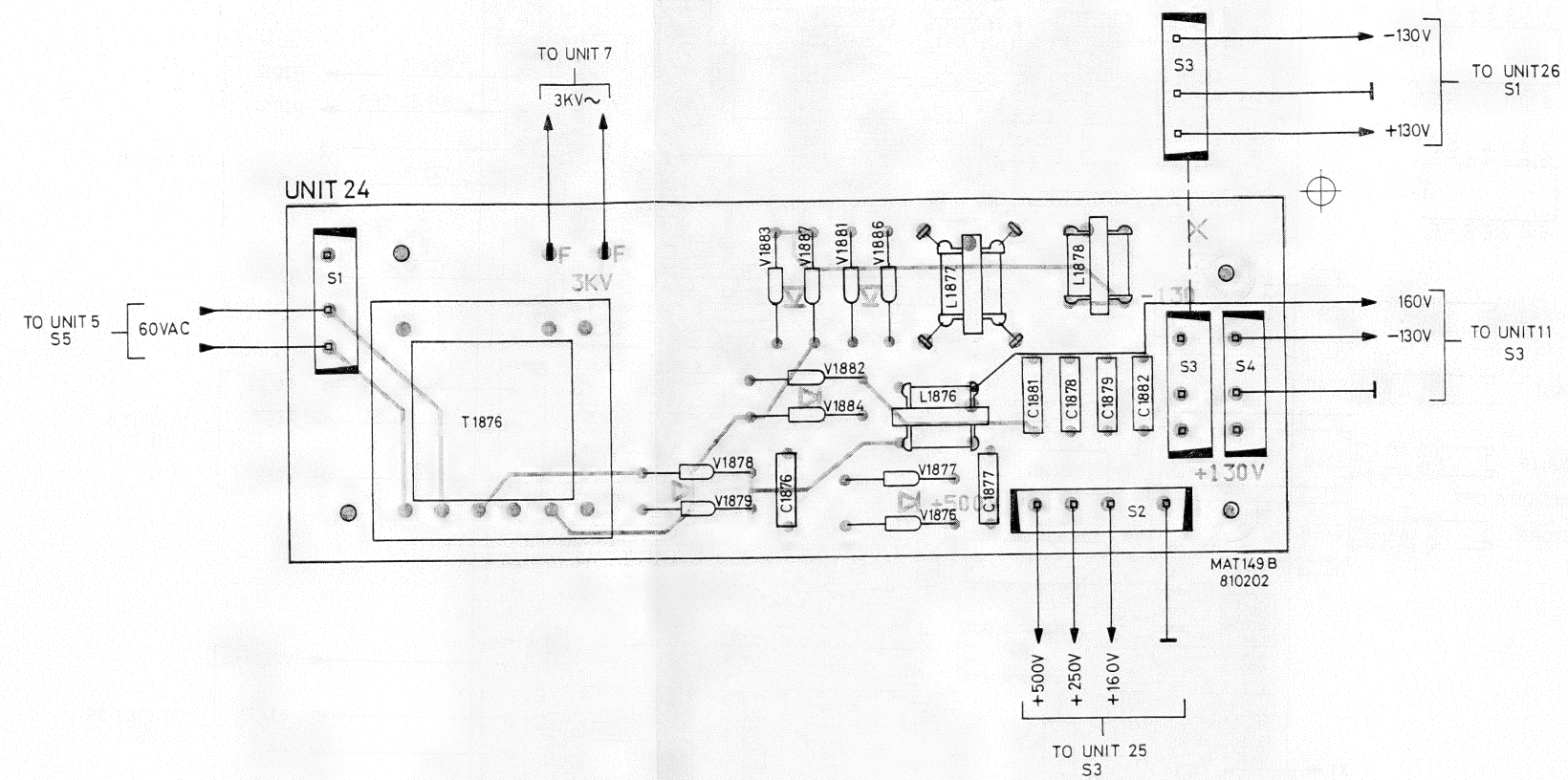


Fig. 12.13. Additional power supply (unit 24), lay-out

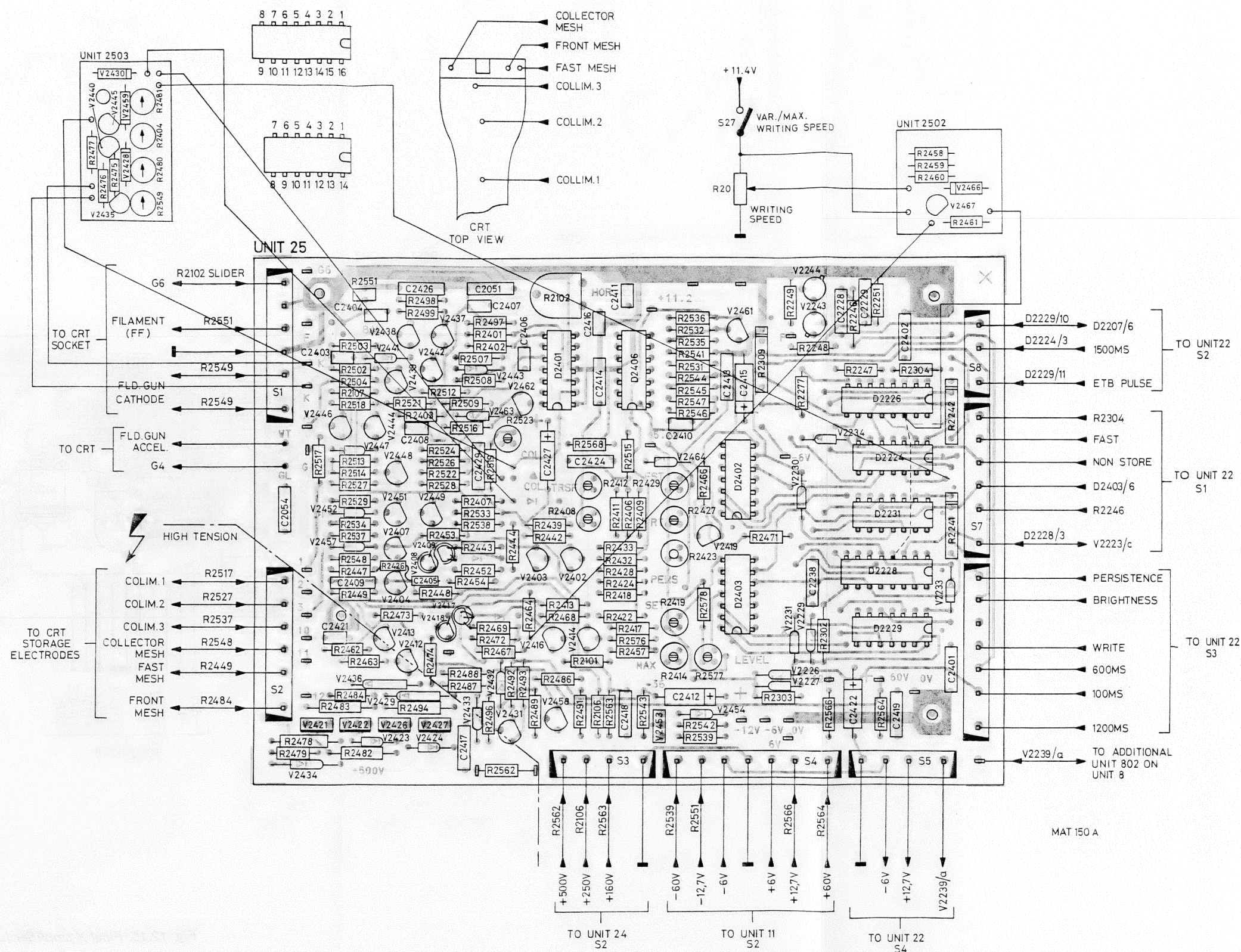
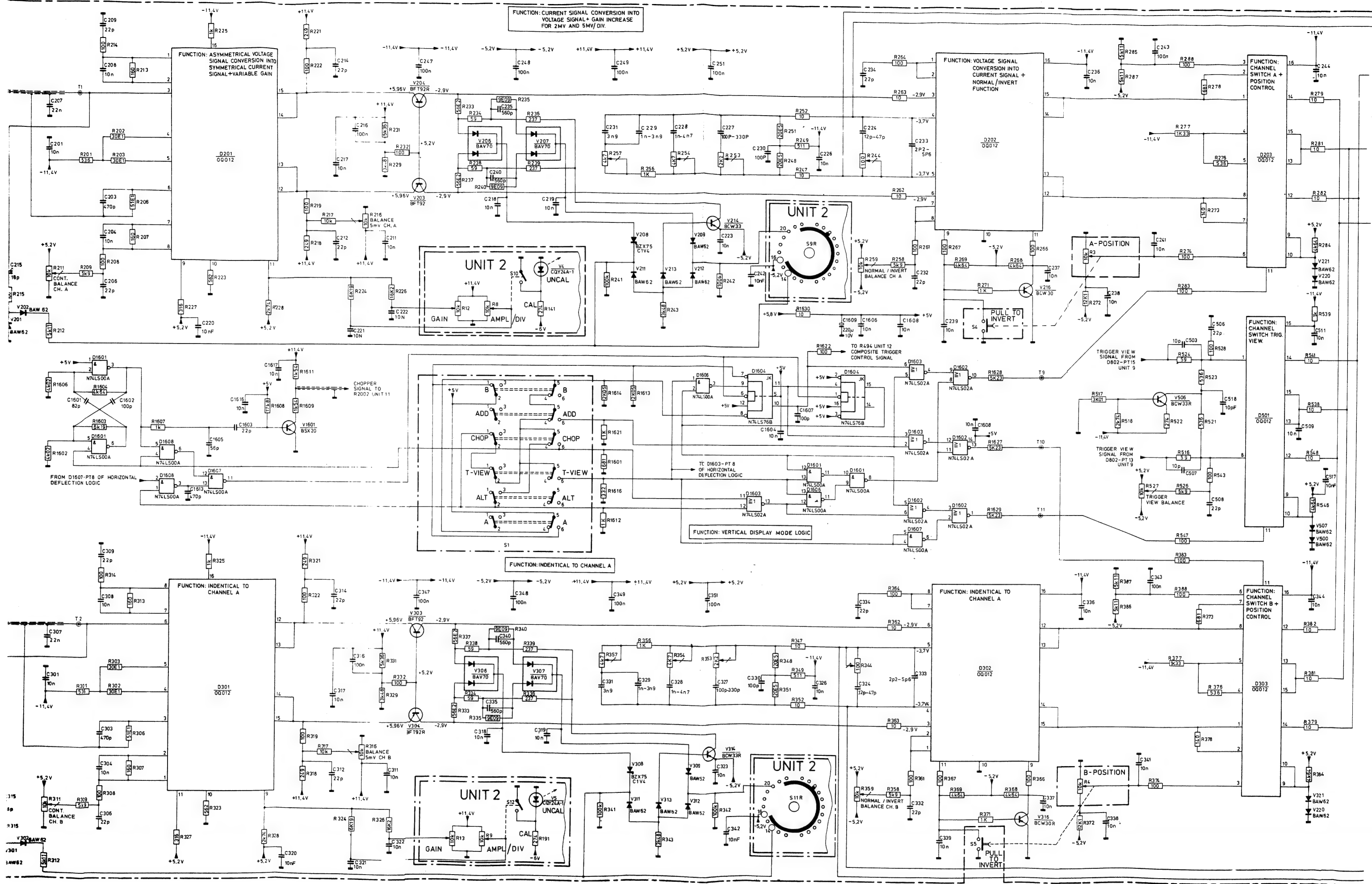


Fig. 12.14. Storage amplifier (unit 25), lay-out



Fig. 12.15. Final X-amplifier (unit 26), lay-out





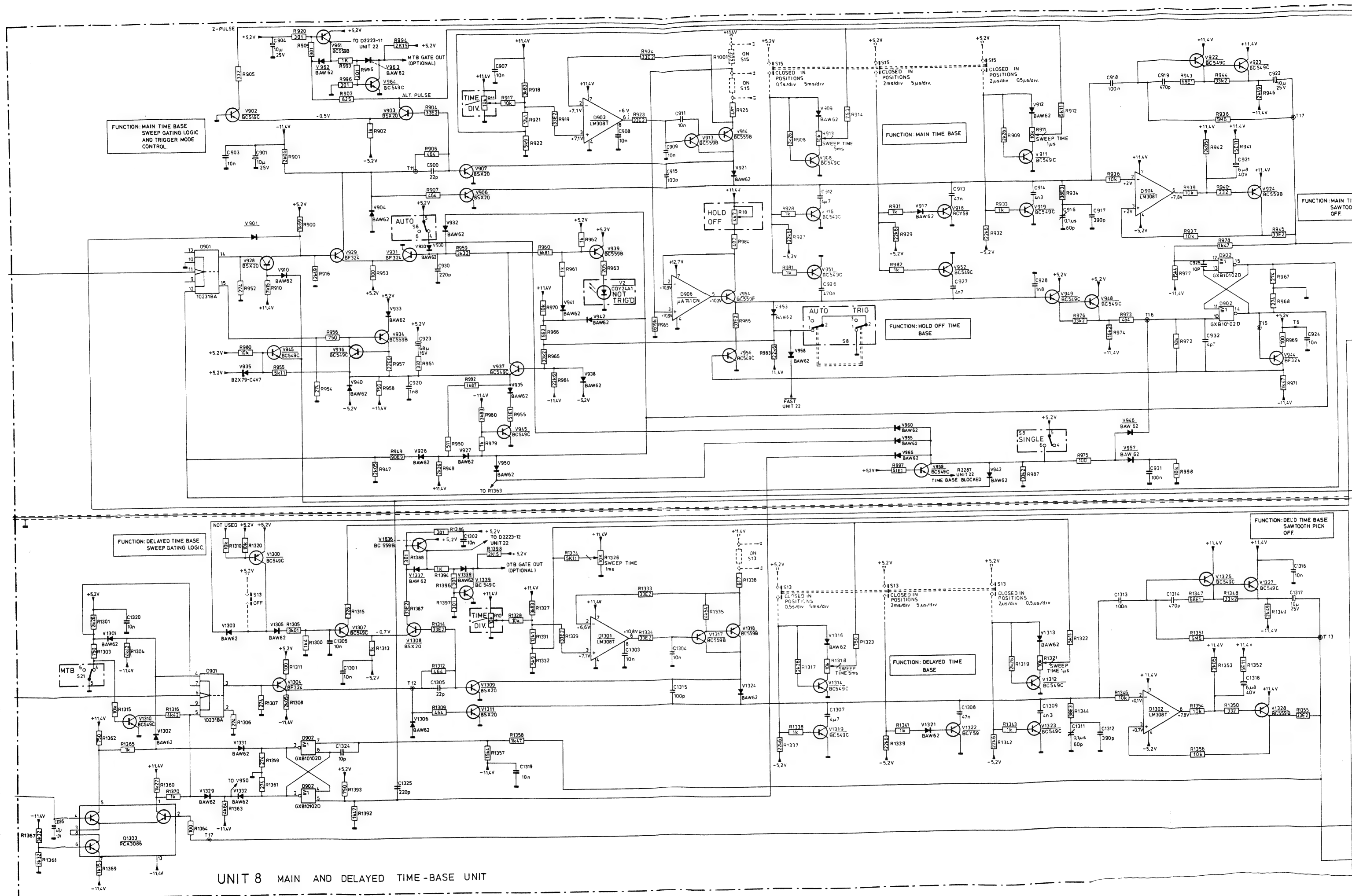
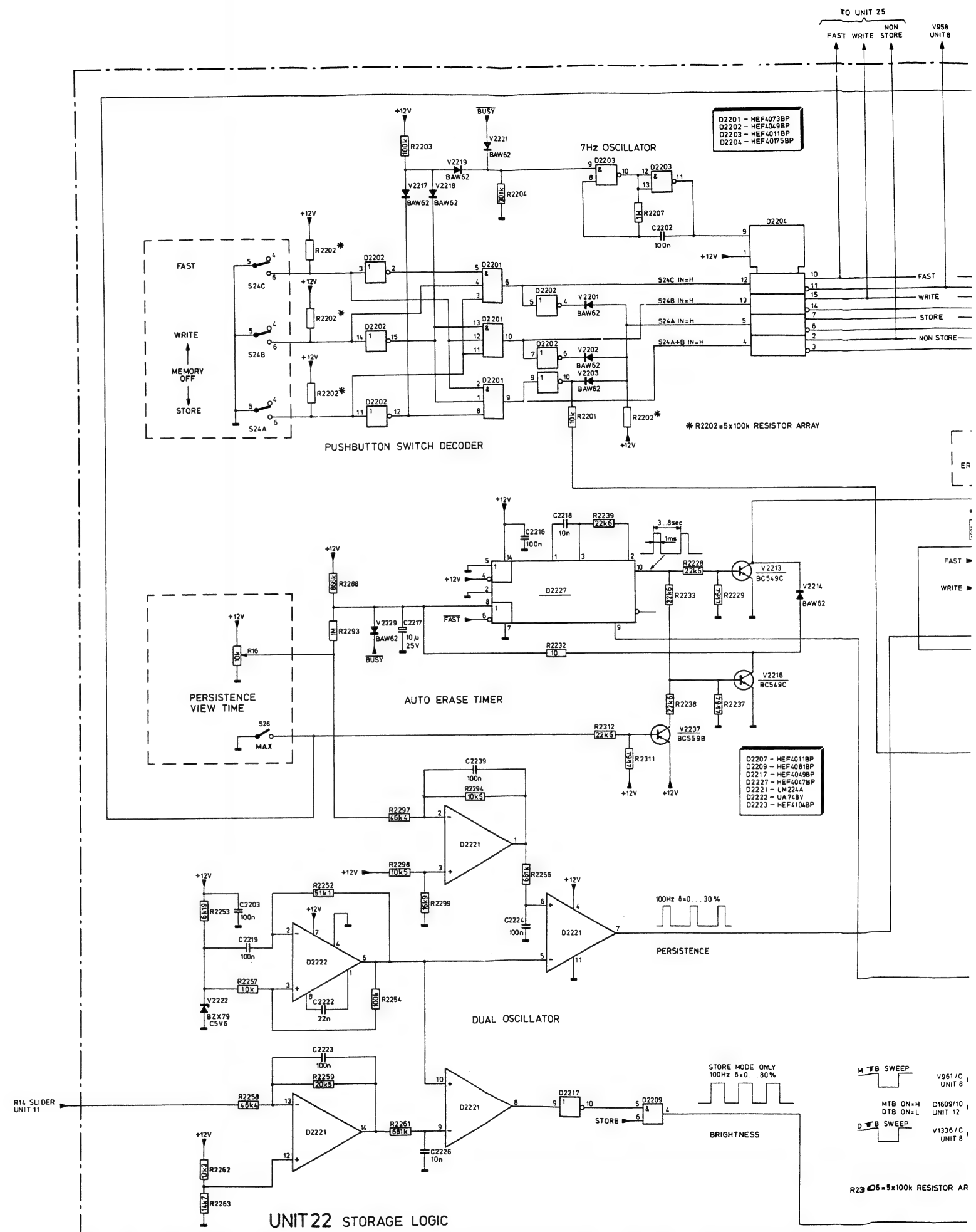


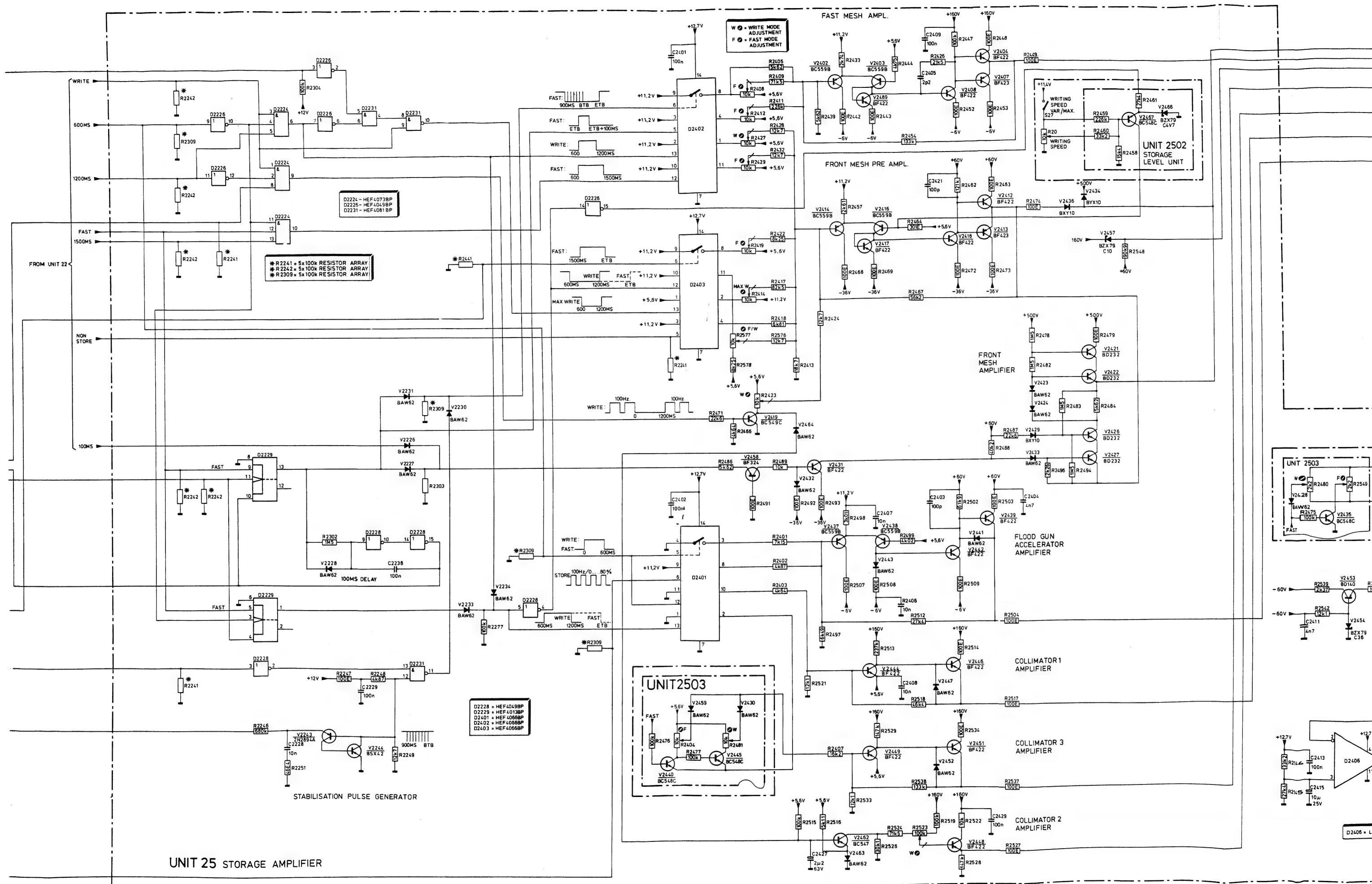
Fig. 12.18. Horizontal deflection, circuit diagram



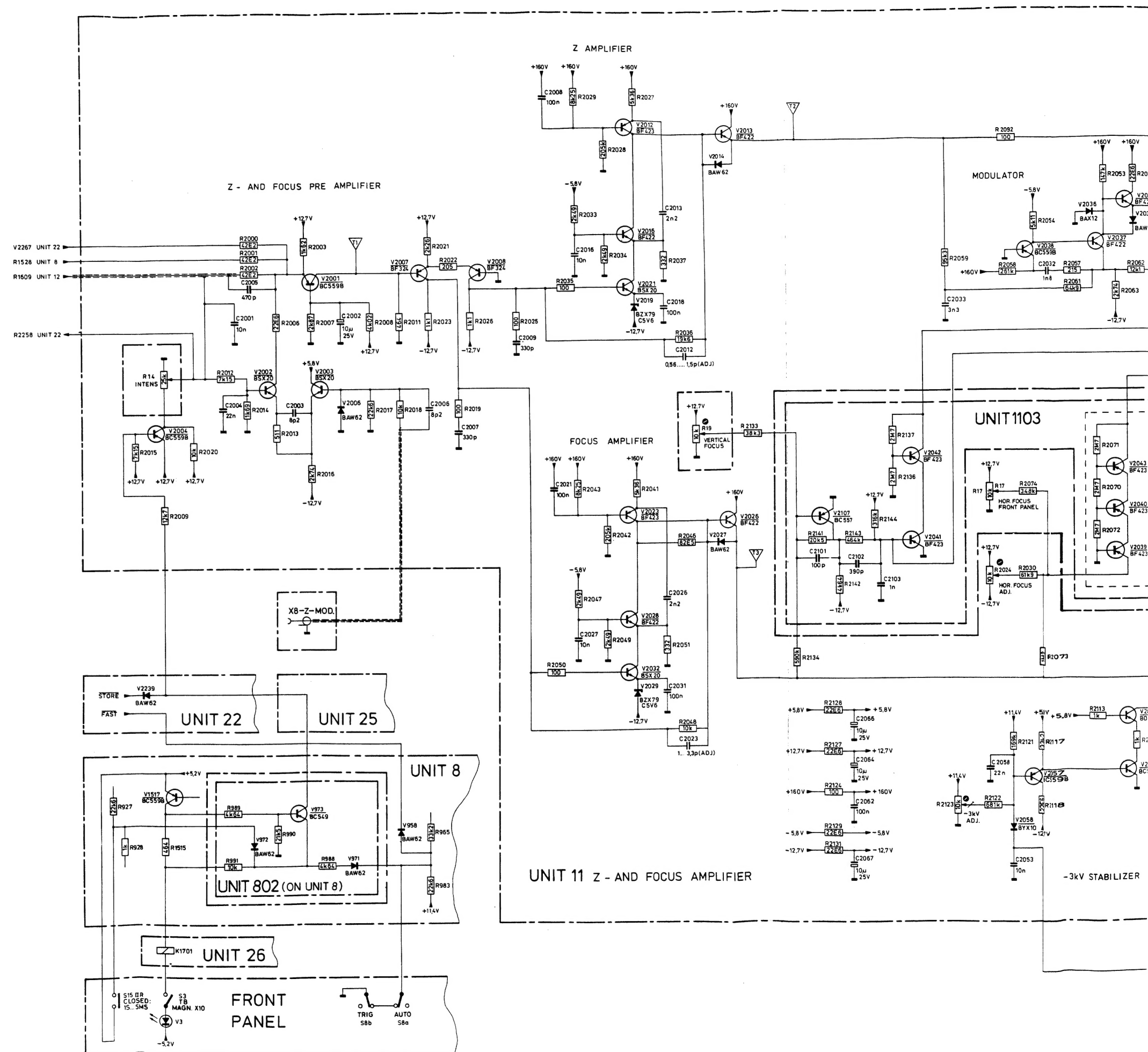
UNIT 9 MTB AND DTB TRIGGER UNIT



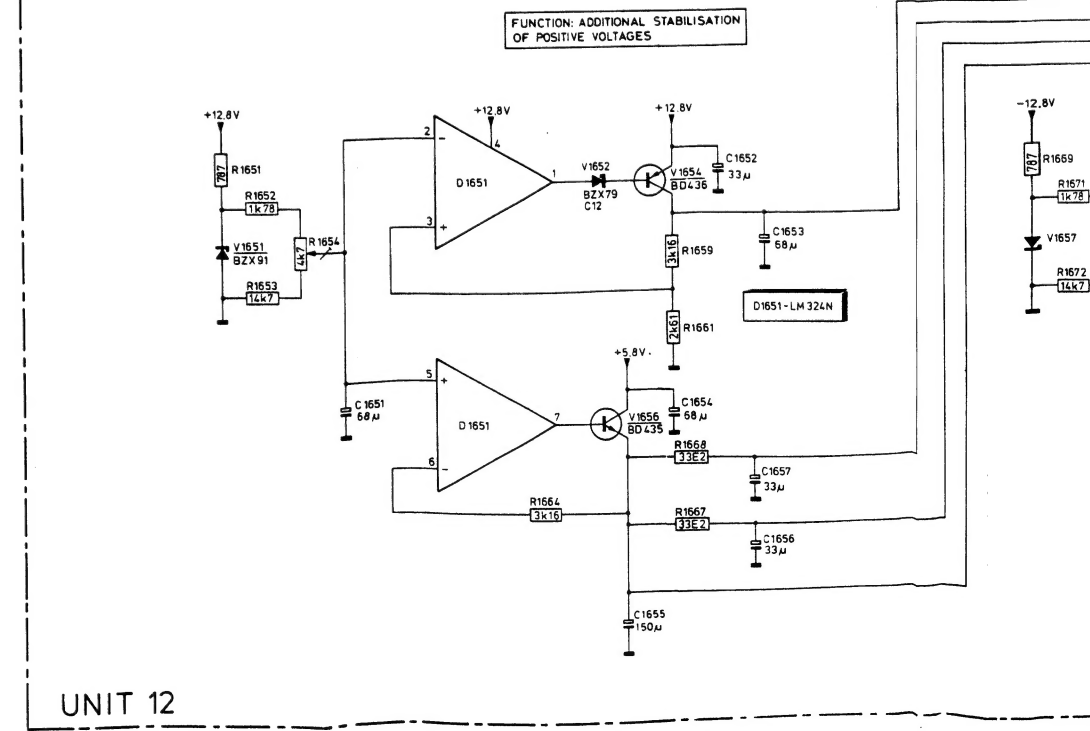
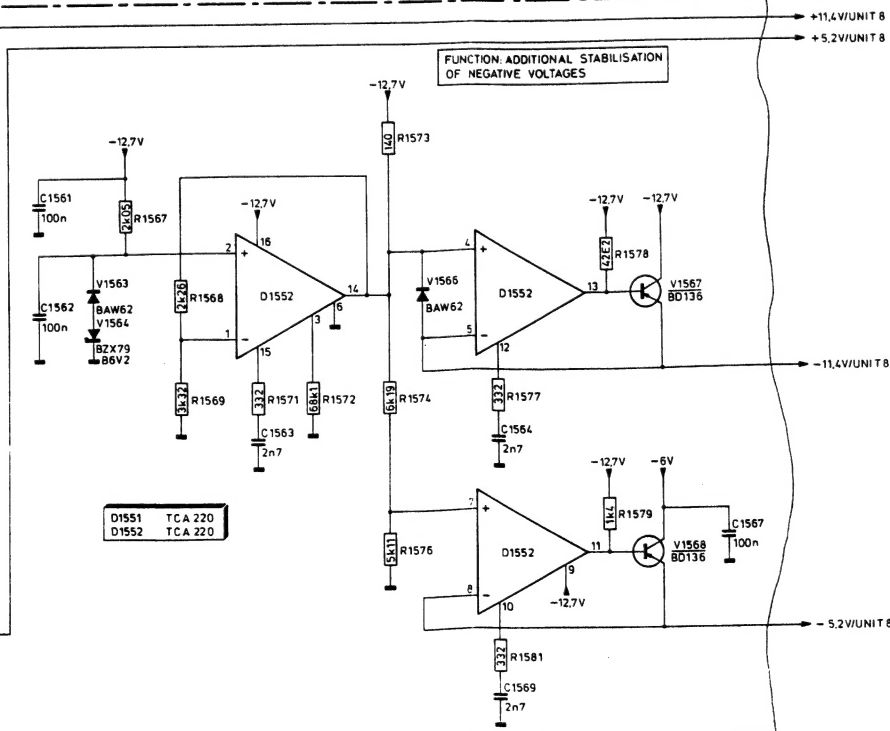
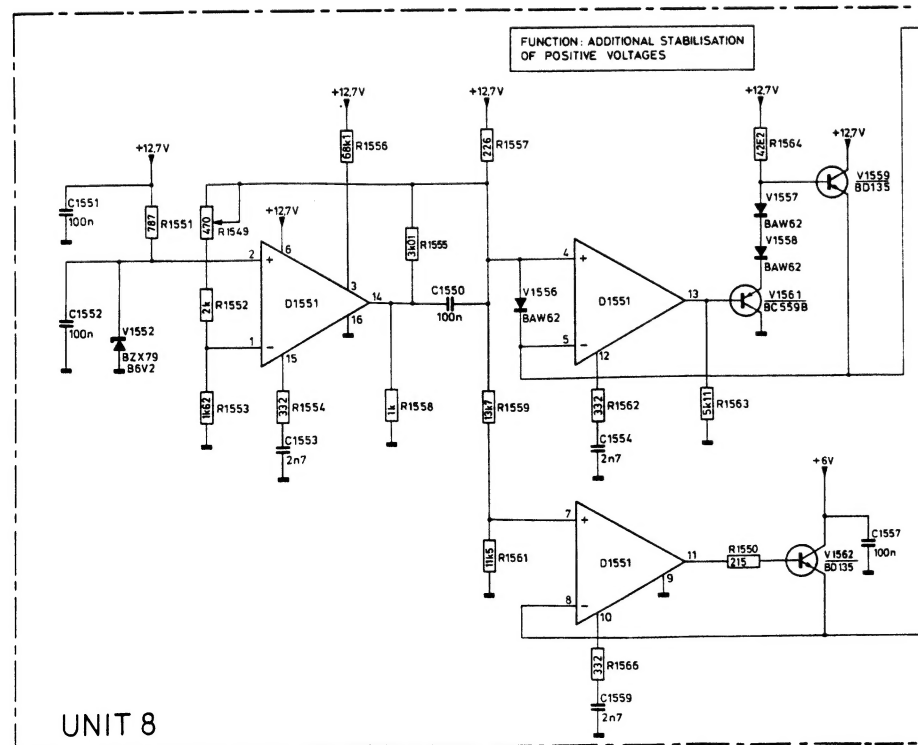
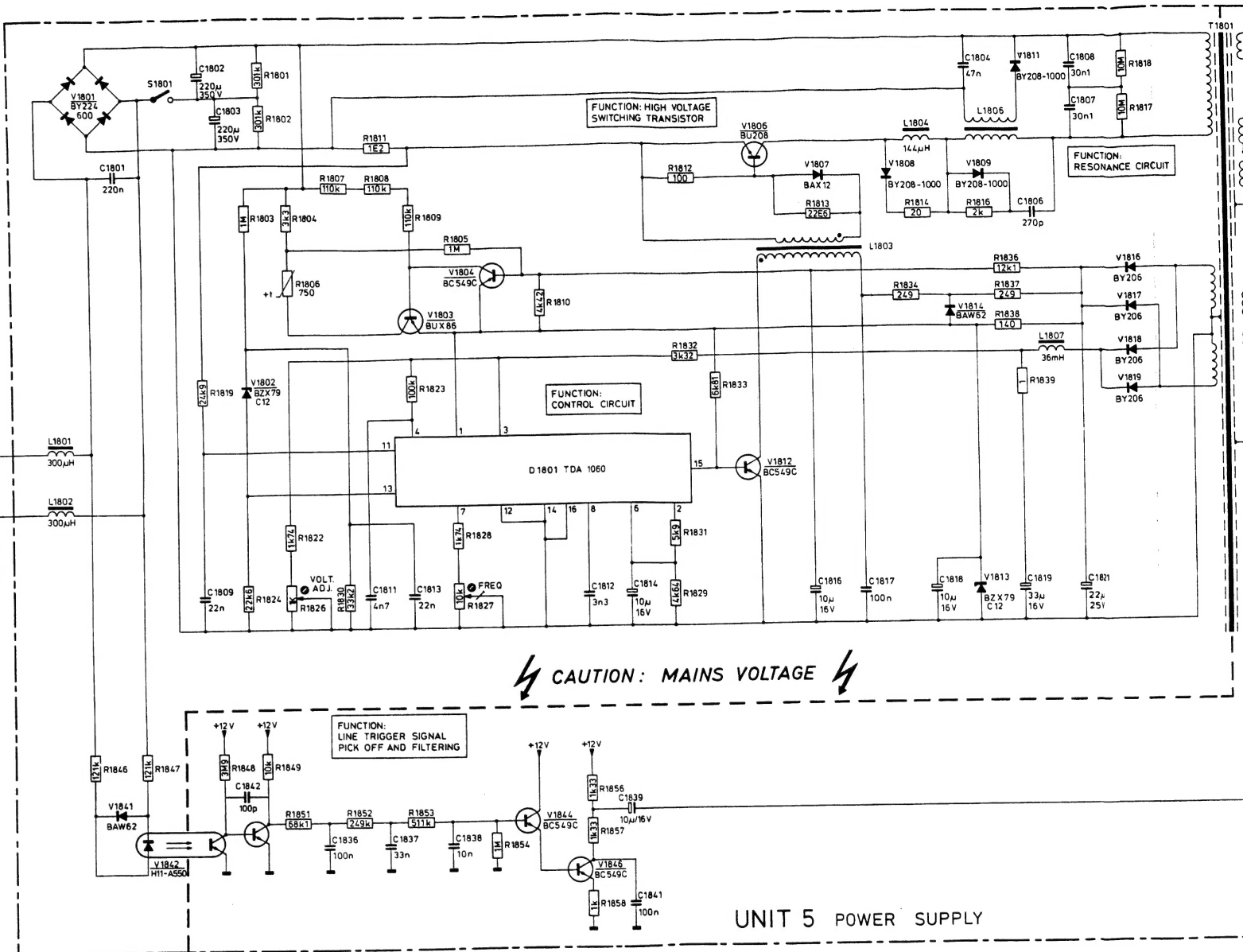
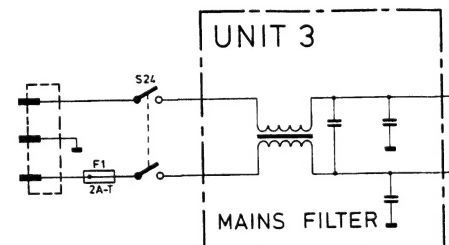
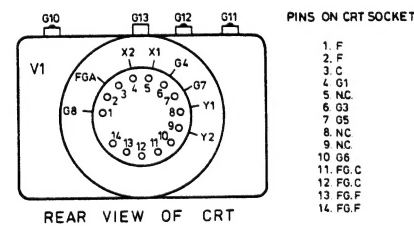
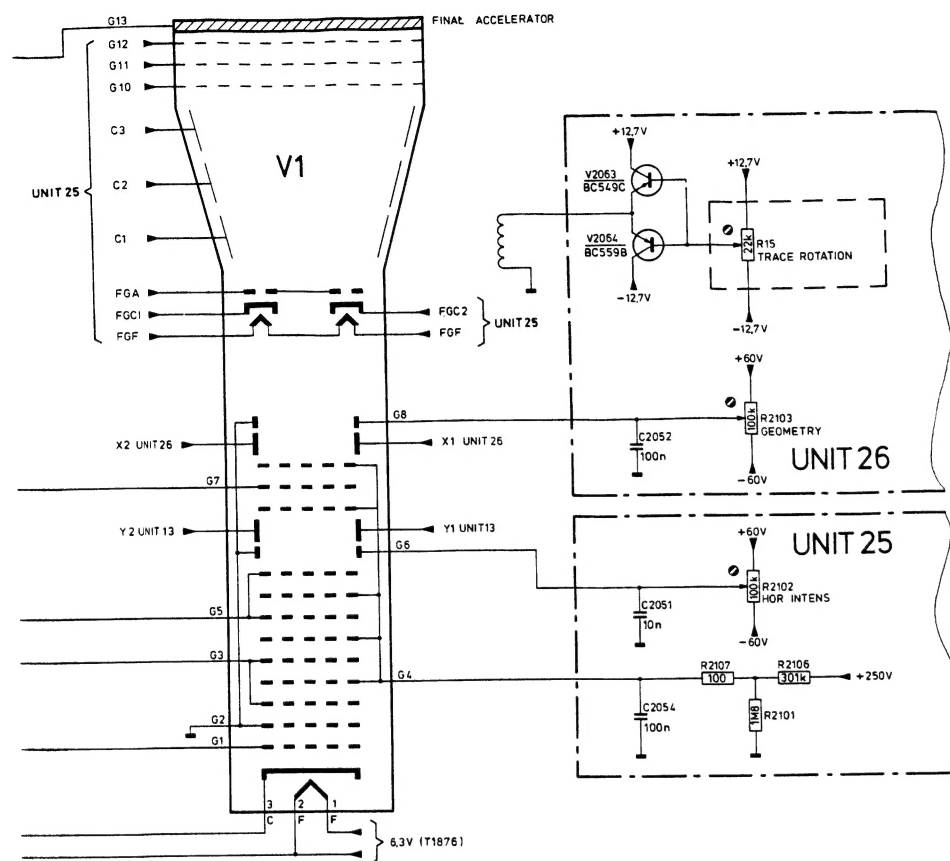




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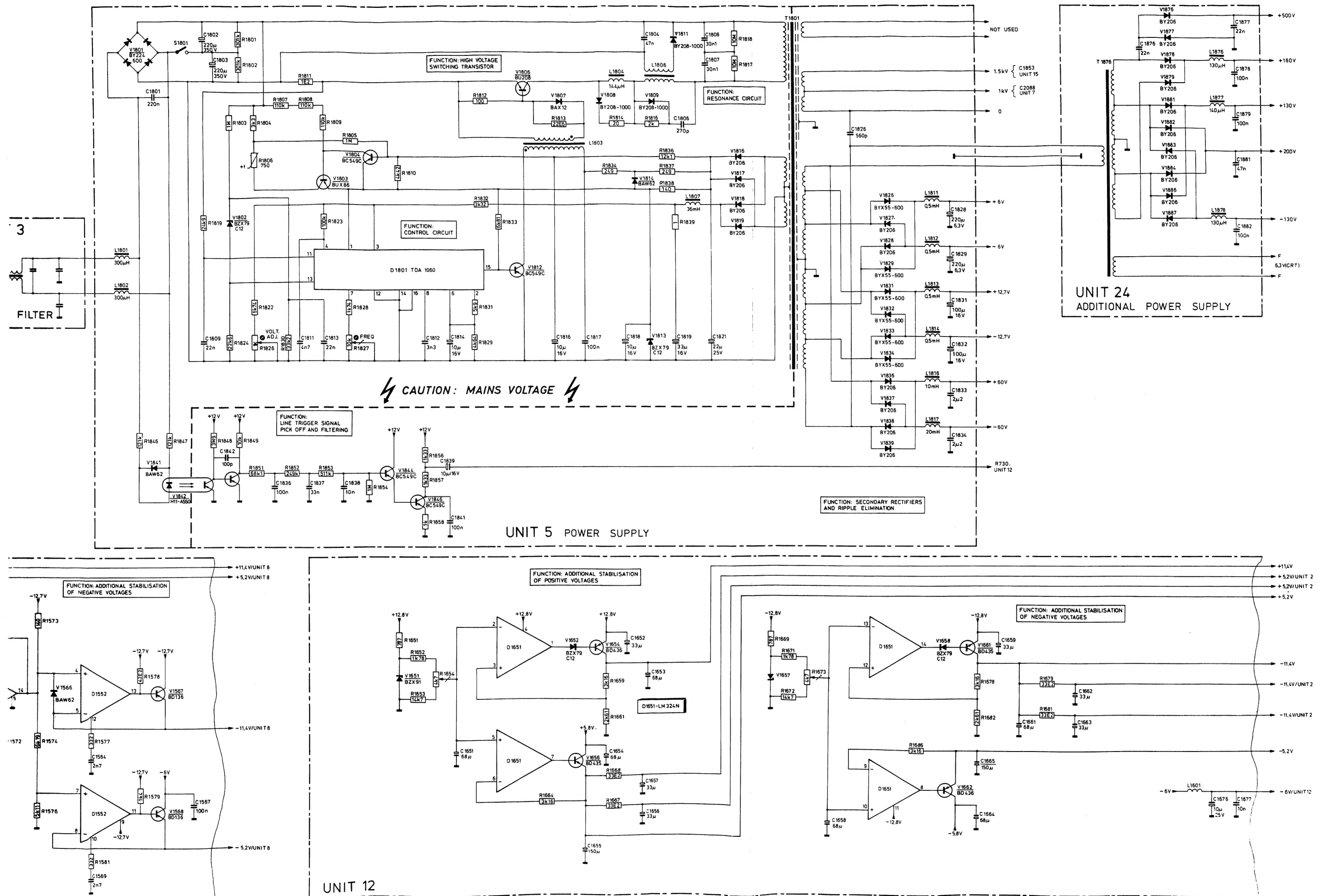


Fig. 12.20. Z-amplifier, focus and power supply, circuit diagram